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図 1 回路

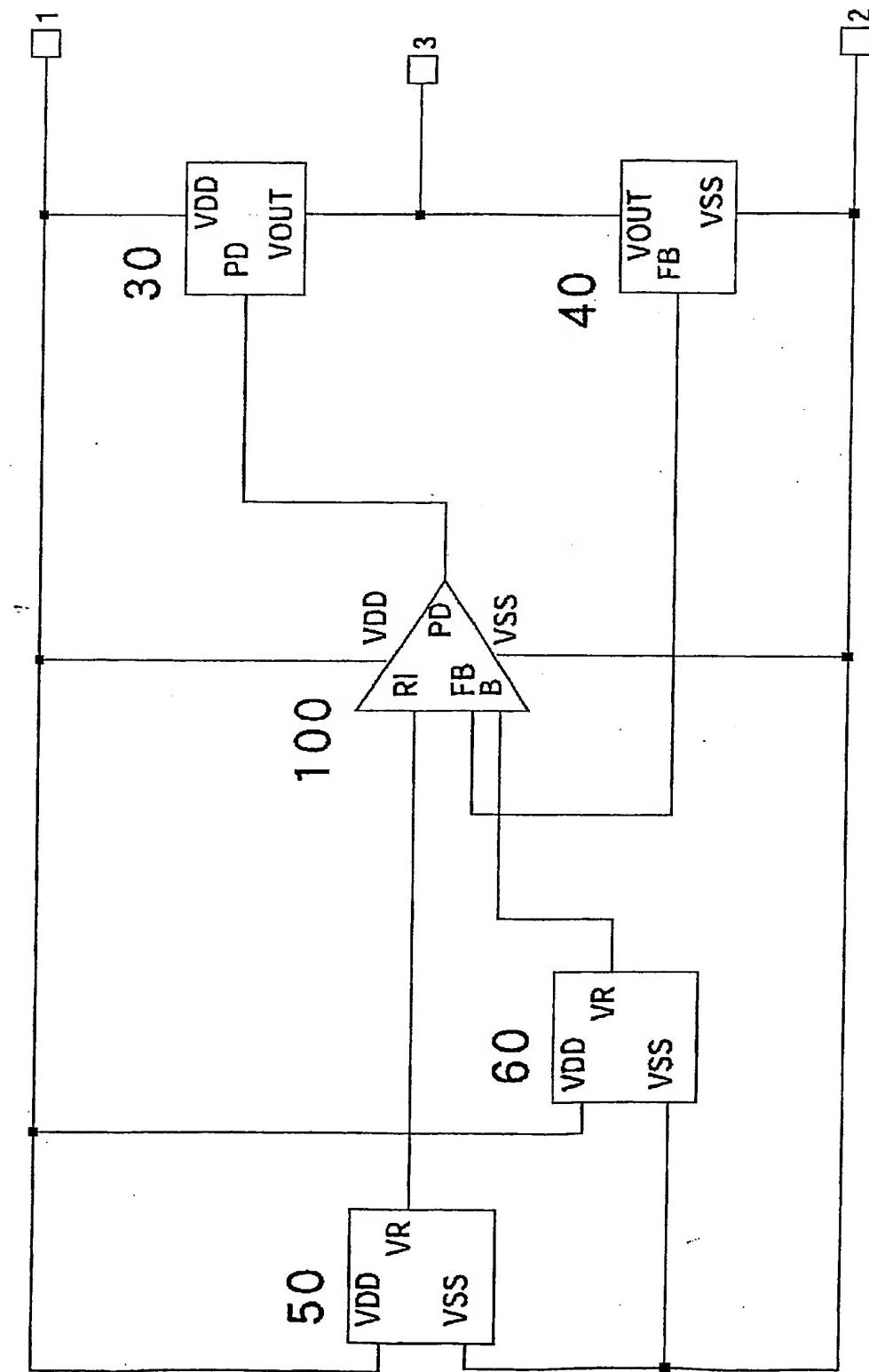
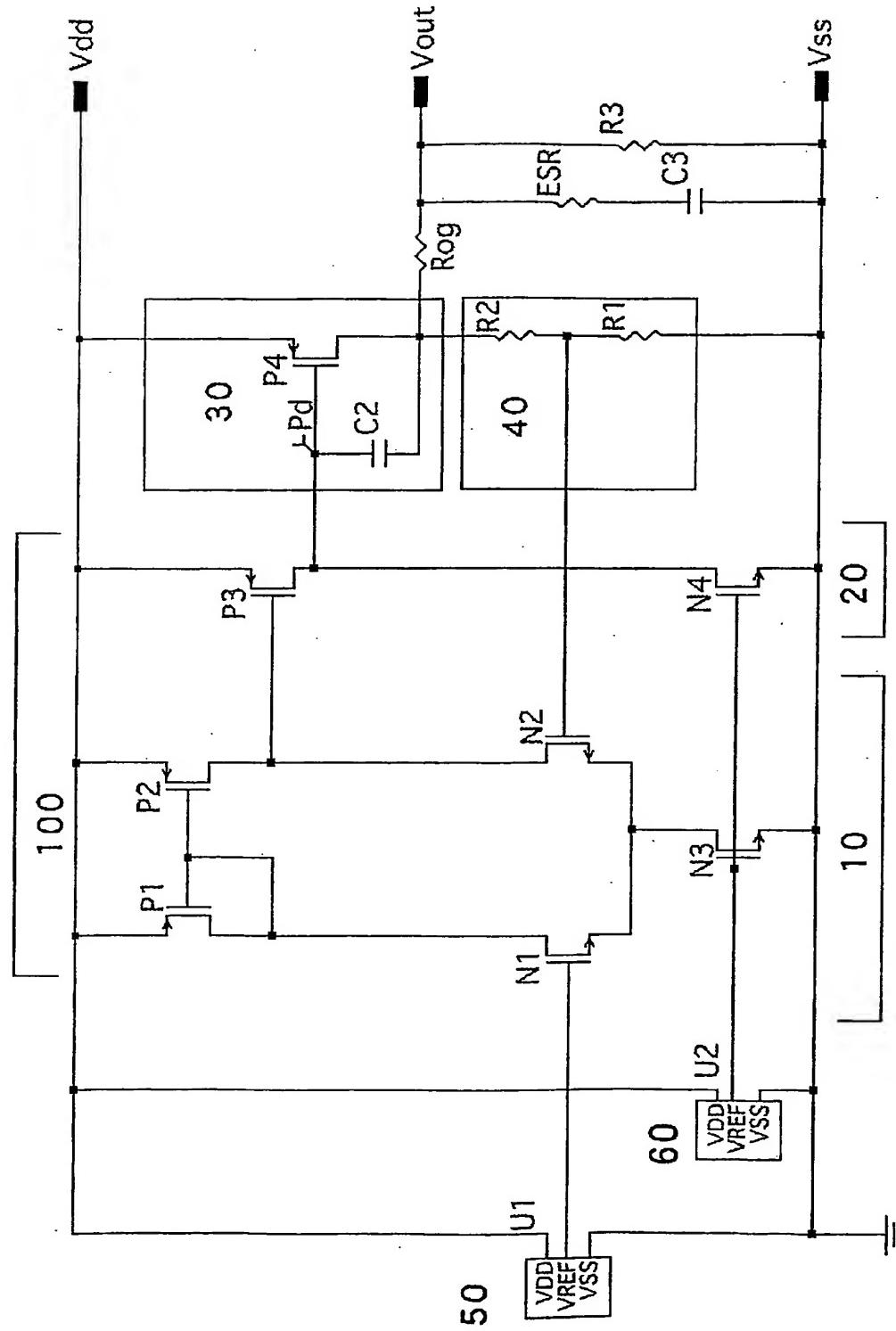


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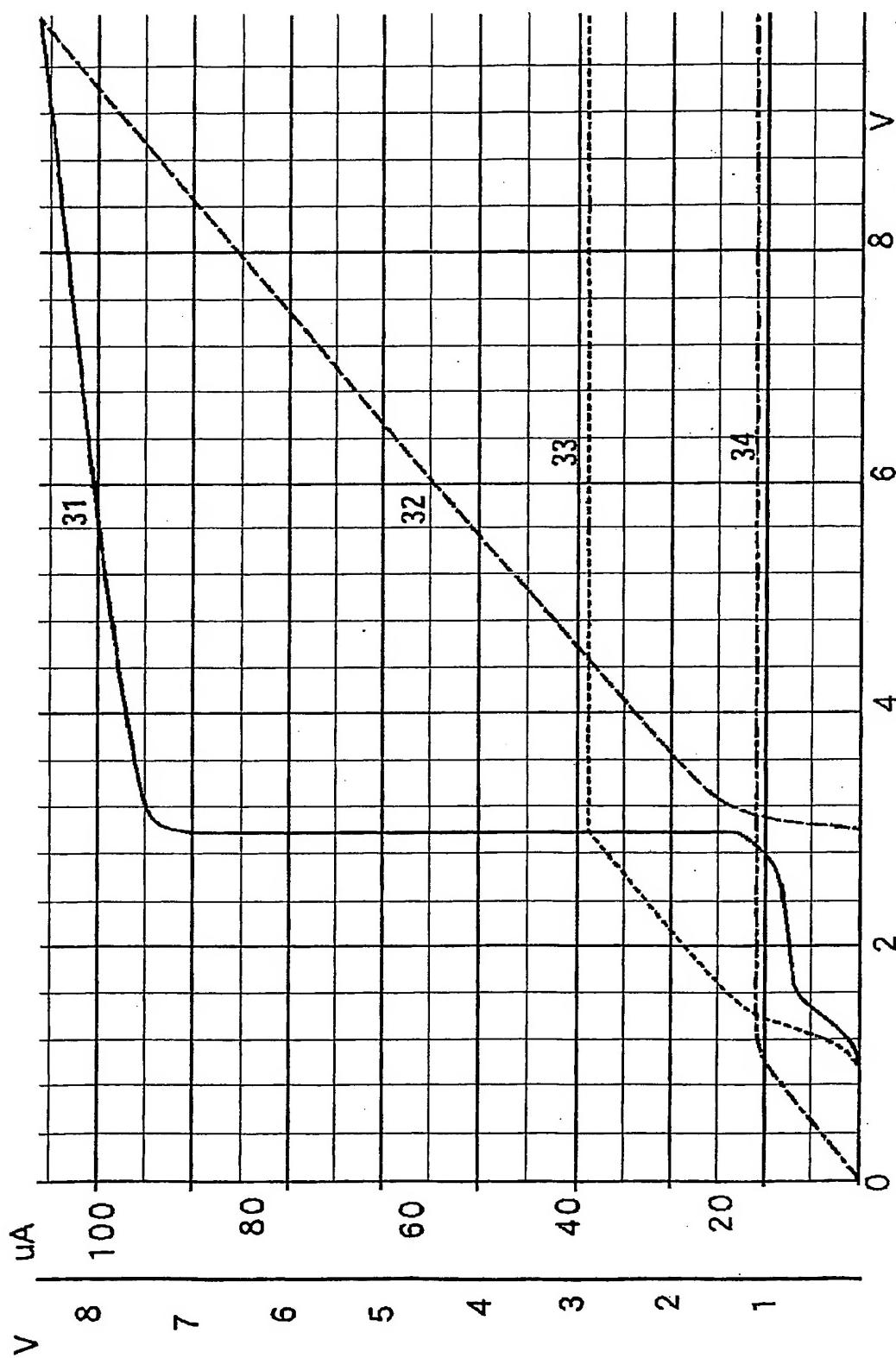


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StdT

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第3図

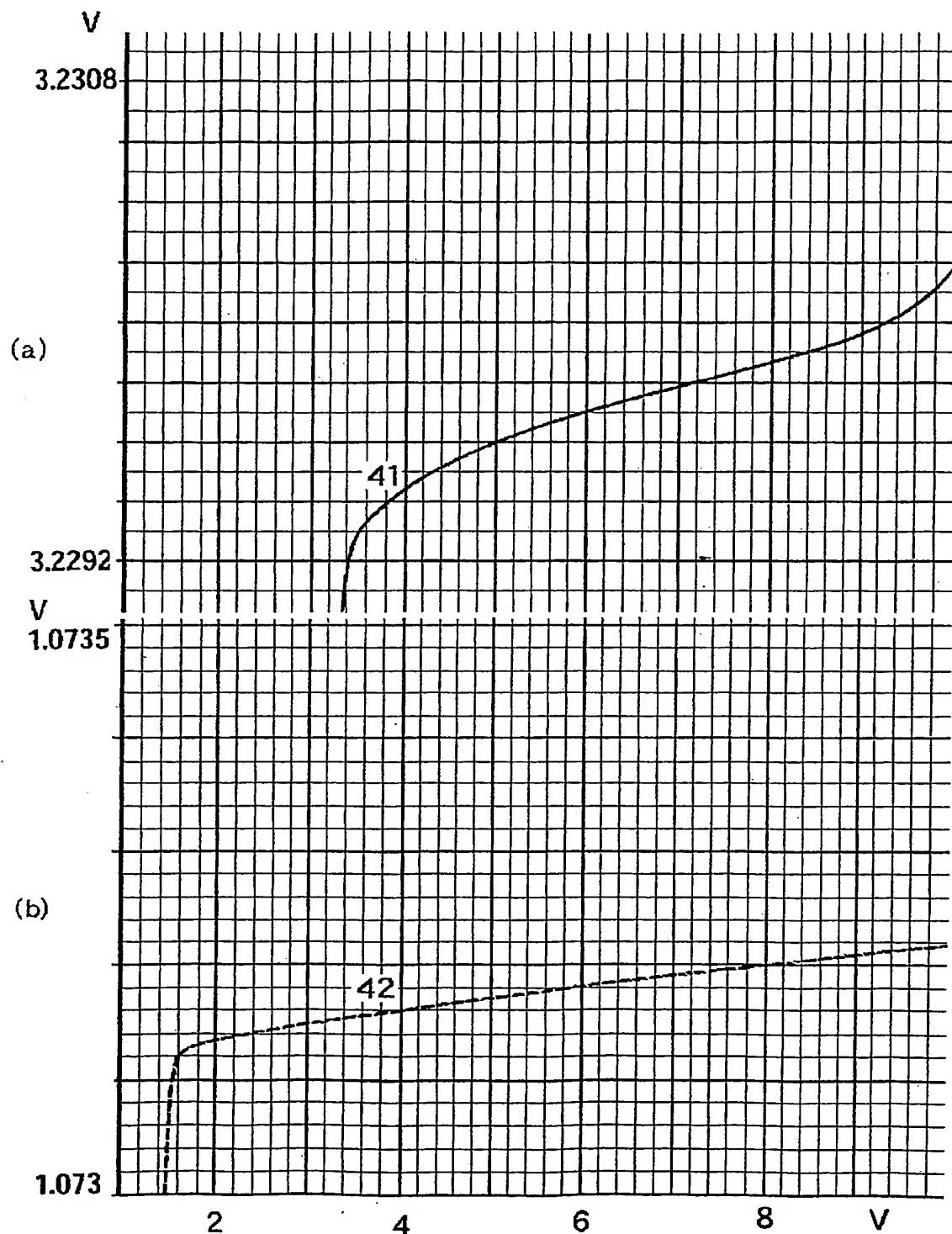


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StdT

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第4図

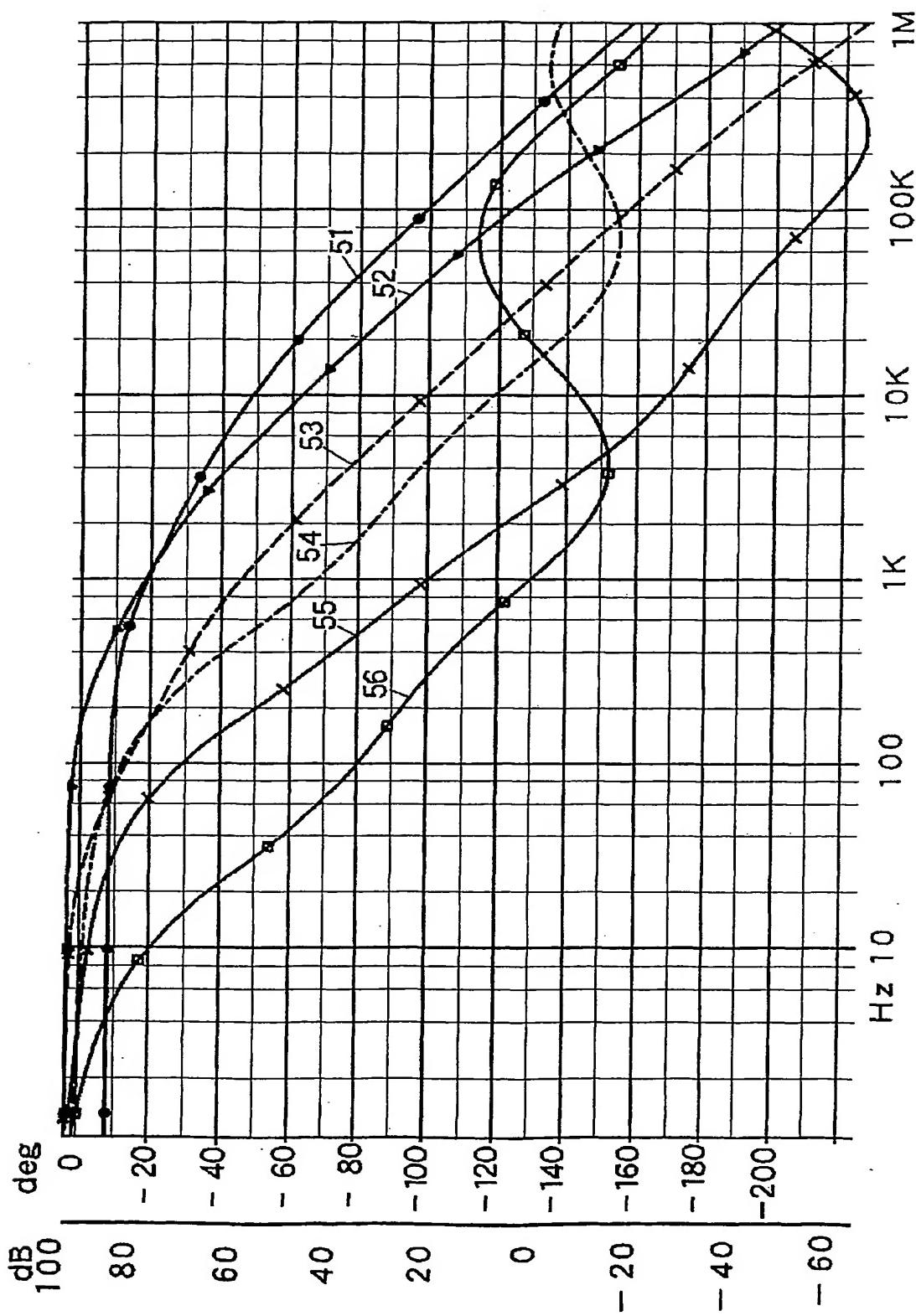


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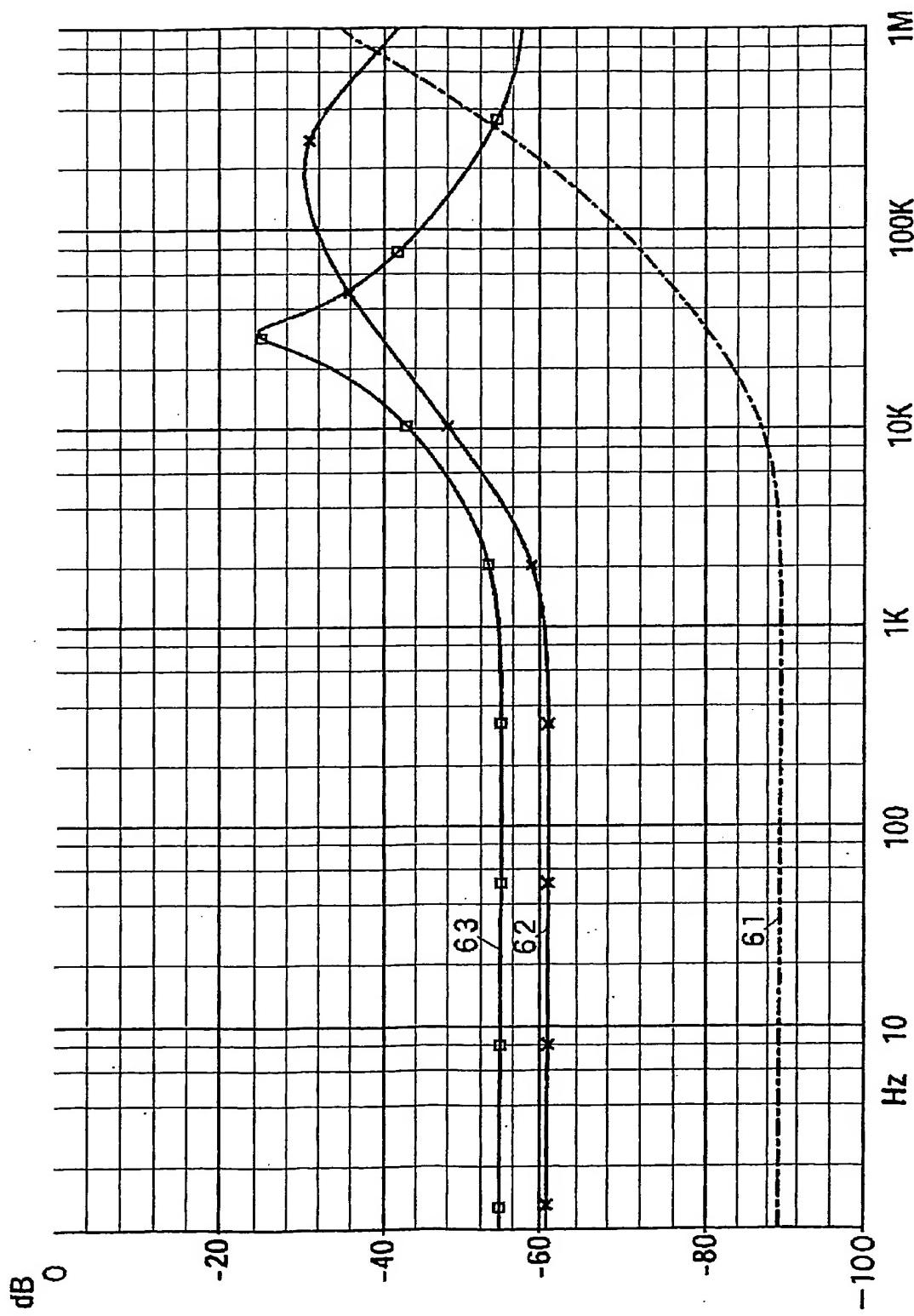
図5



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第6図



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第 7 図

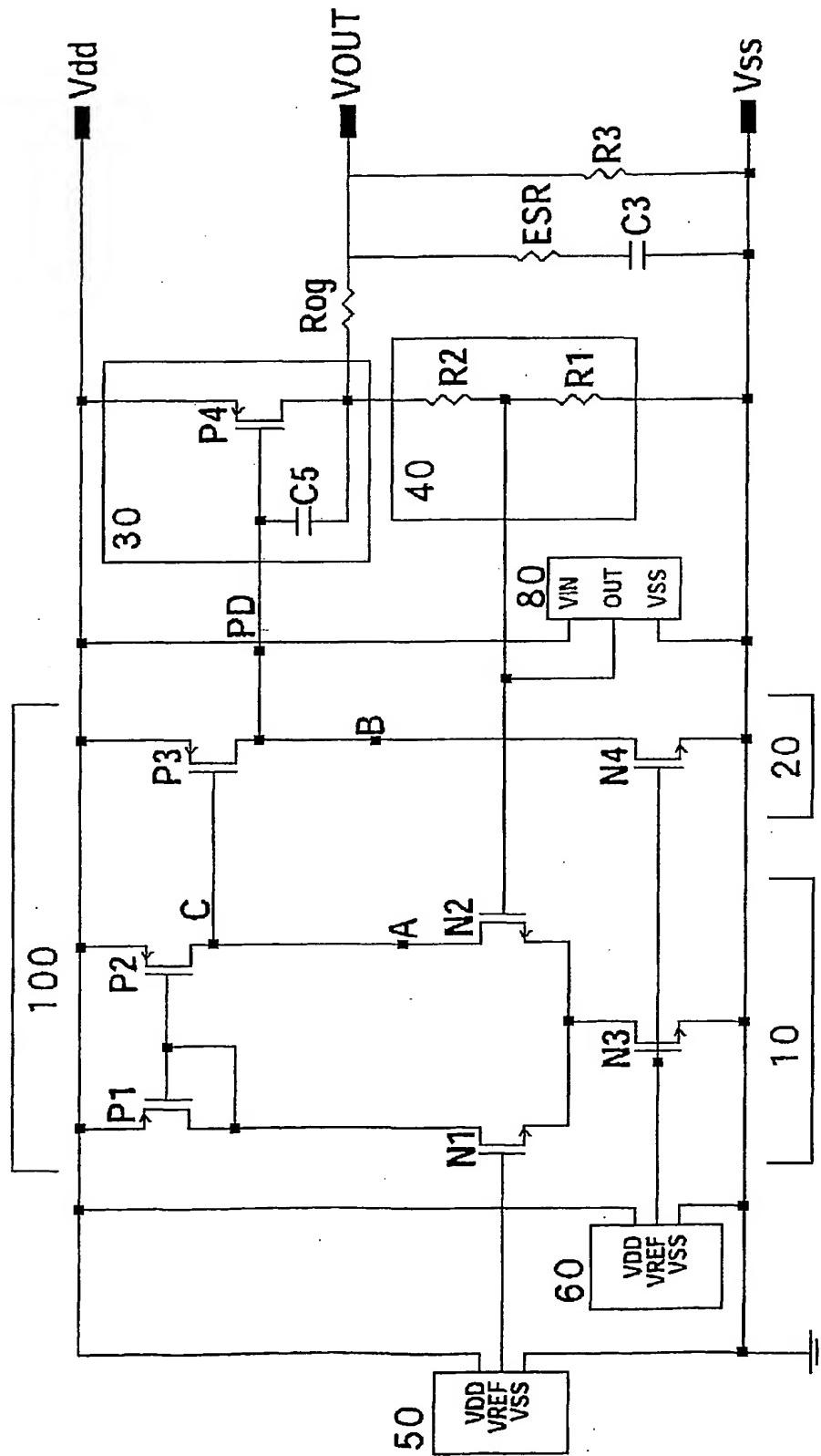
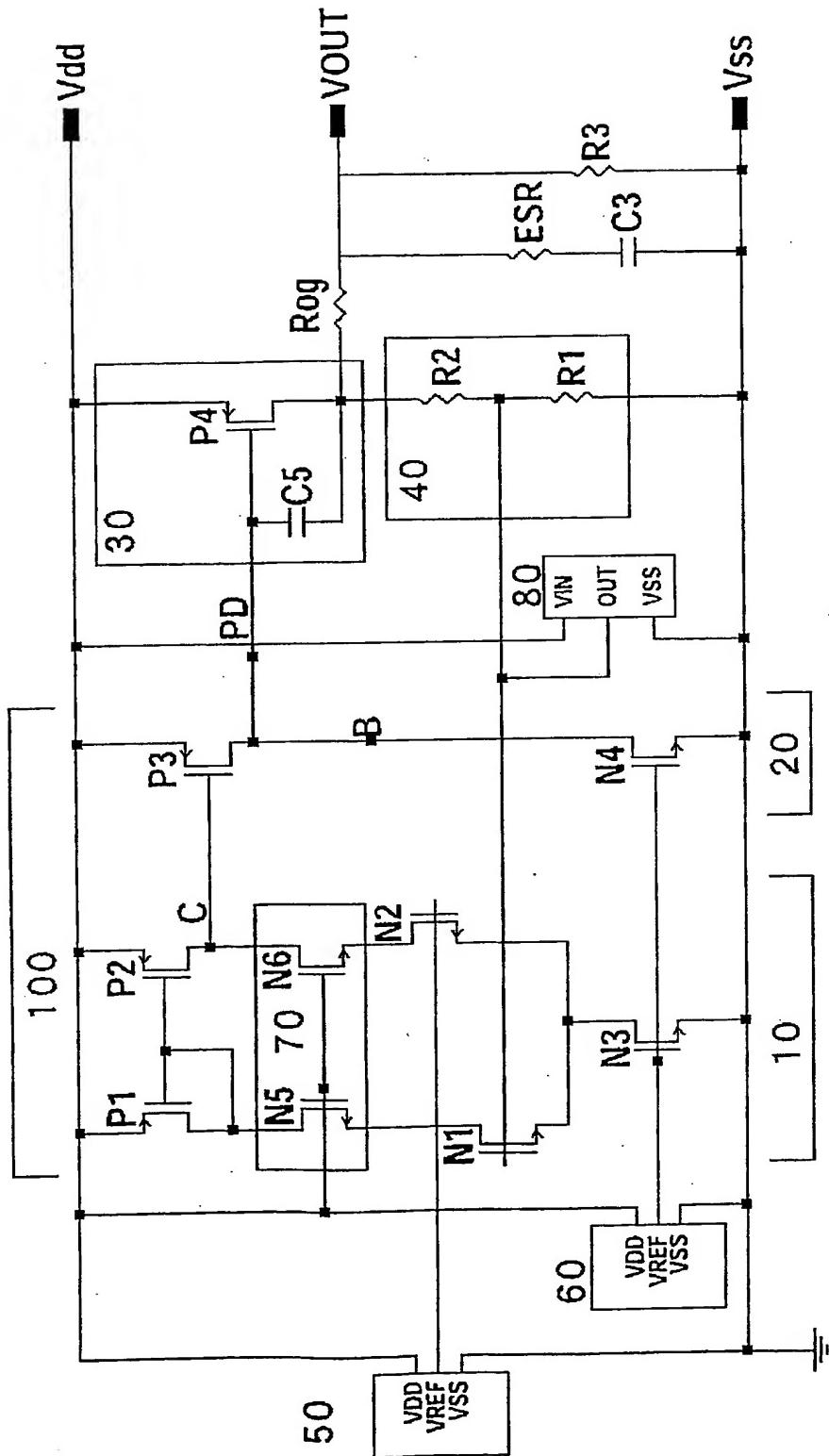


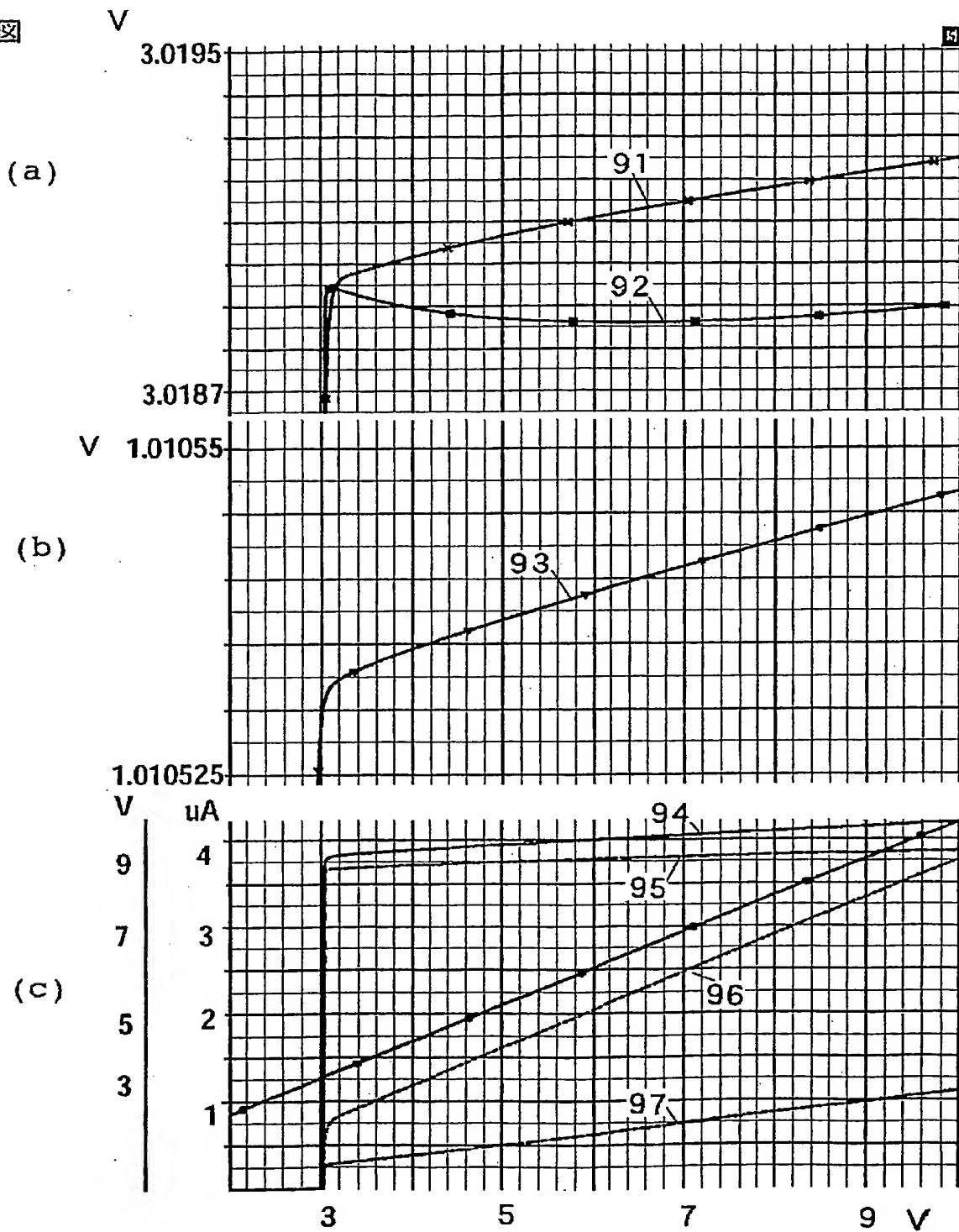
図 8



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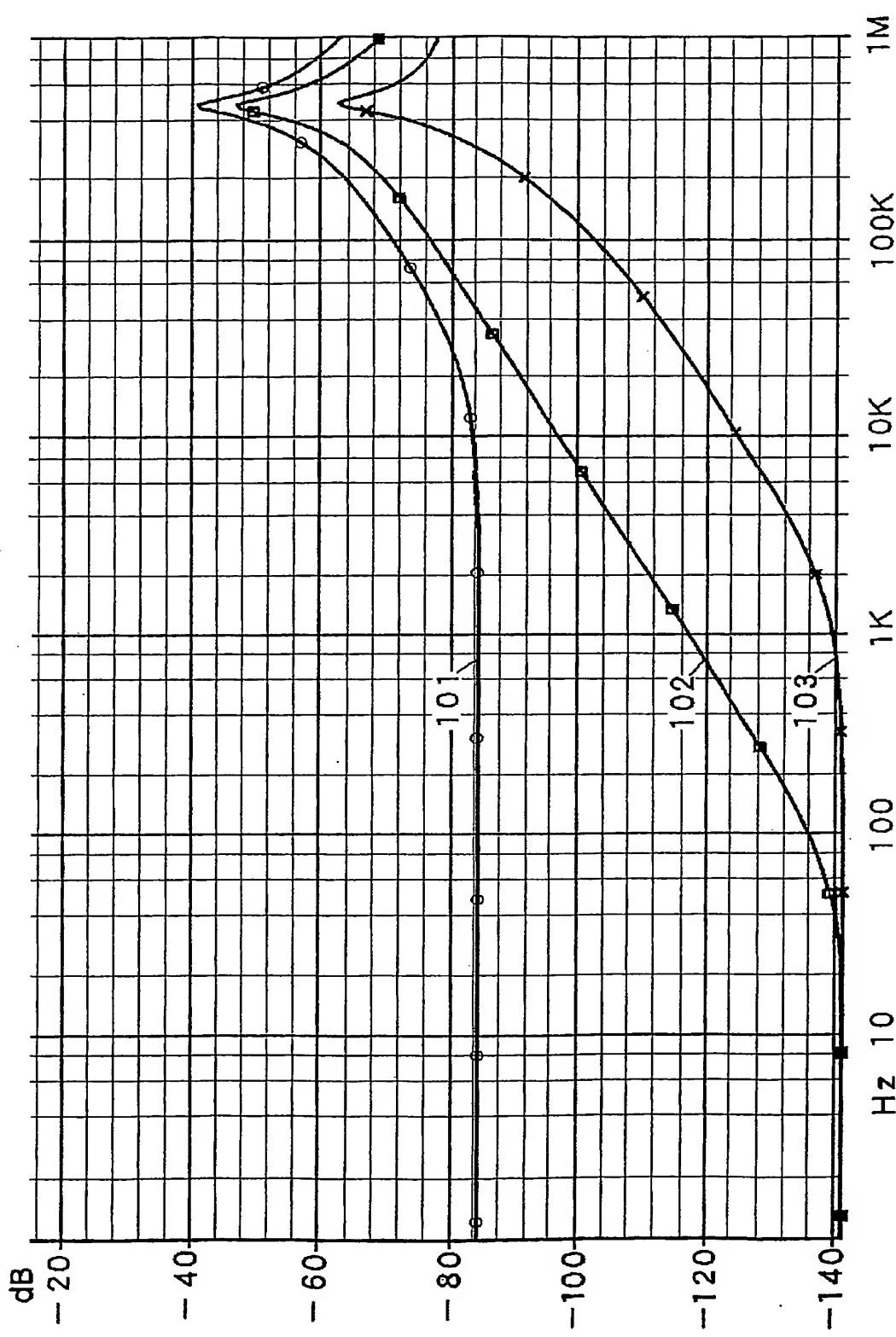
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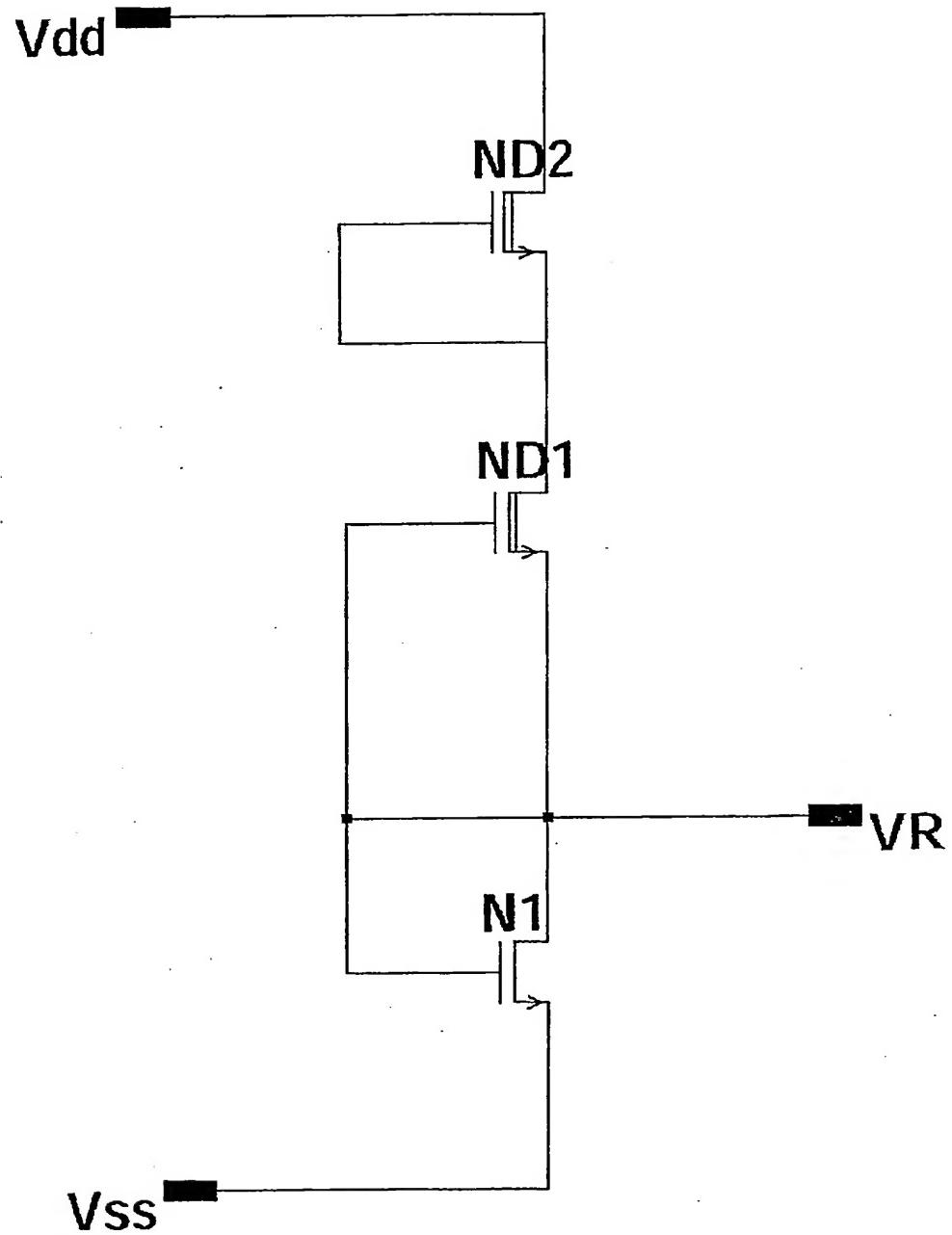
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第10図



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第11図



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第12図

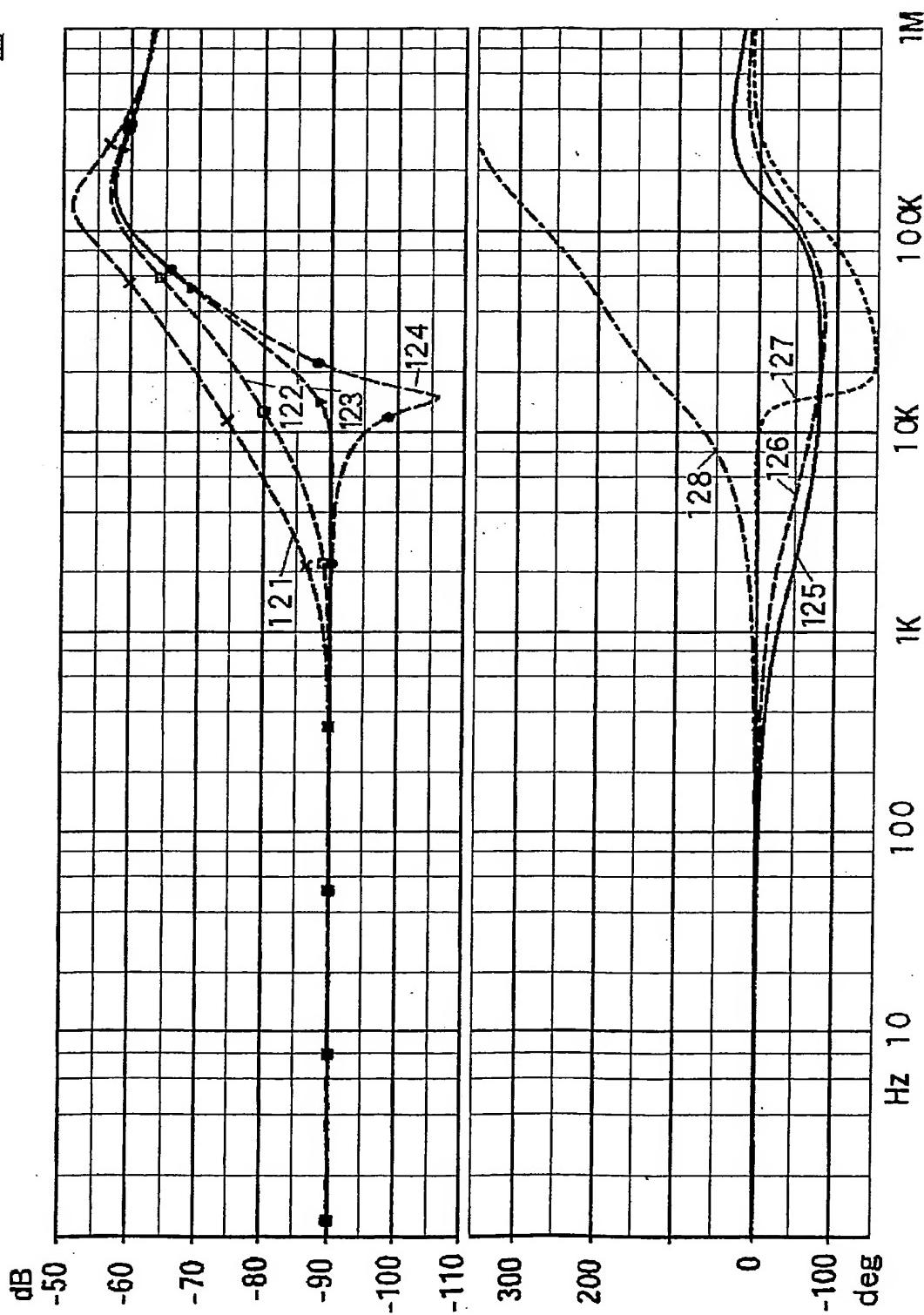
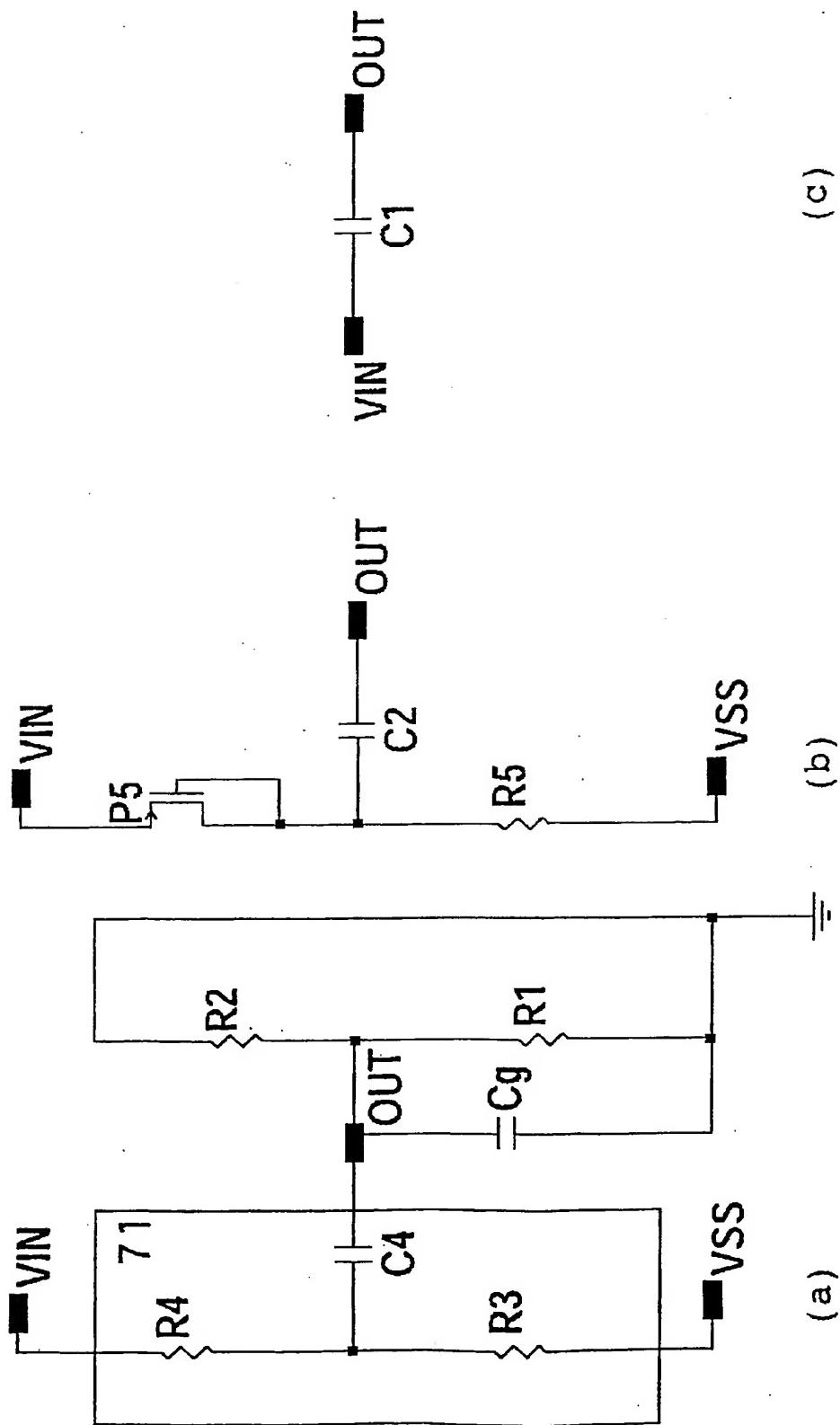
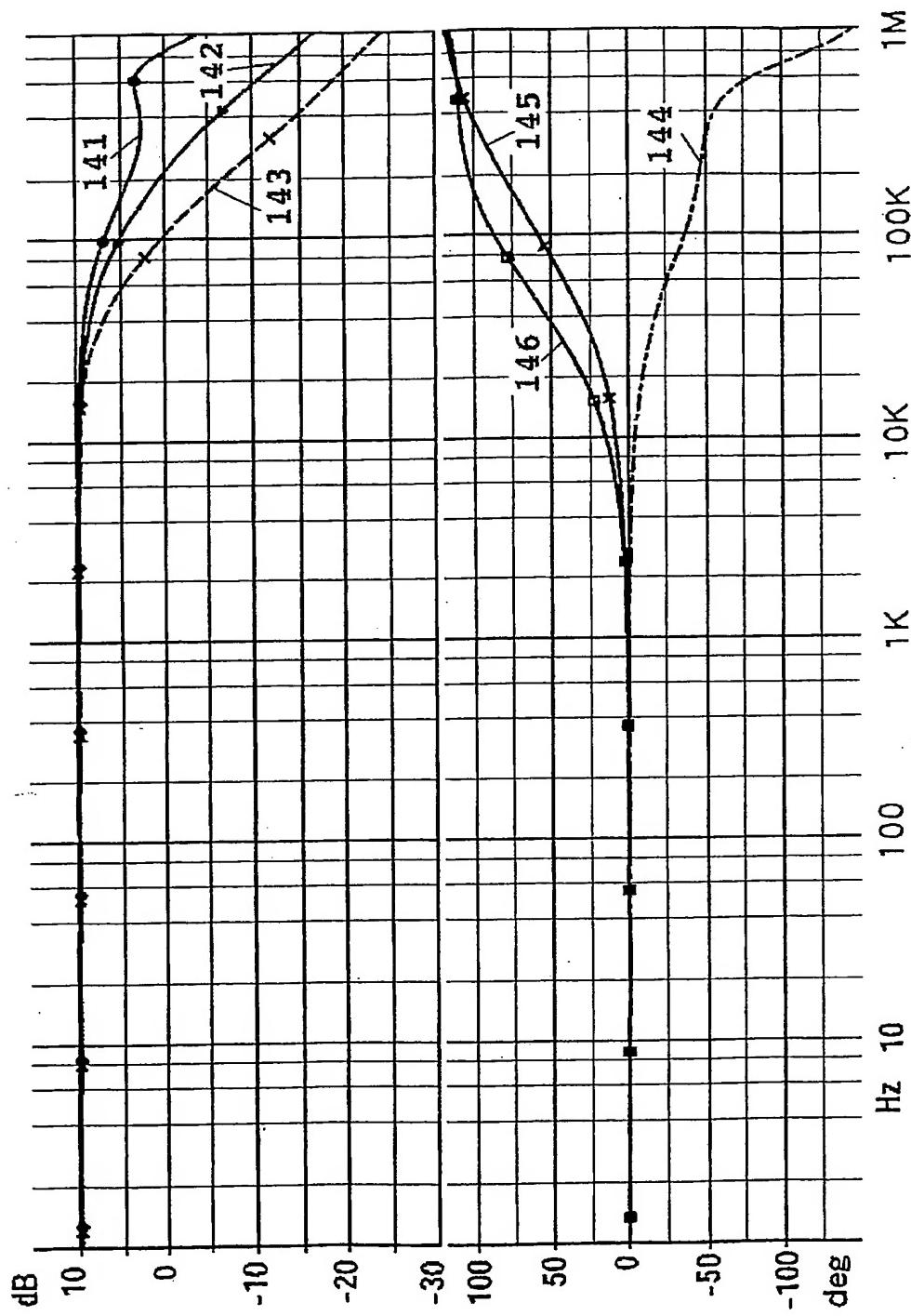


図 1-3



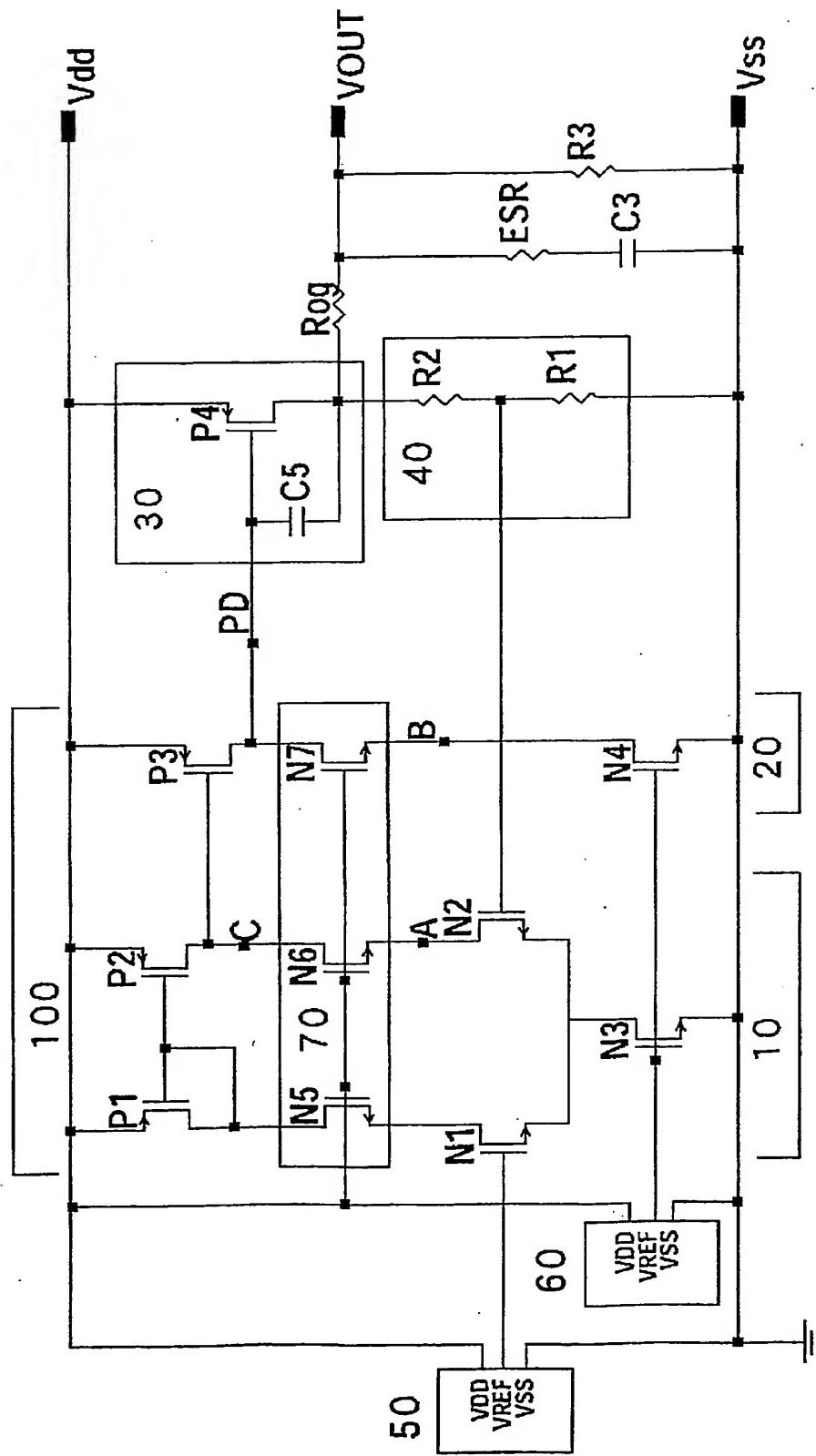
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第14図



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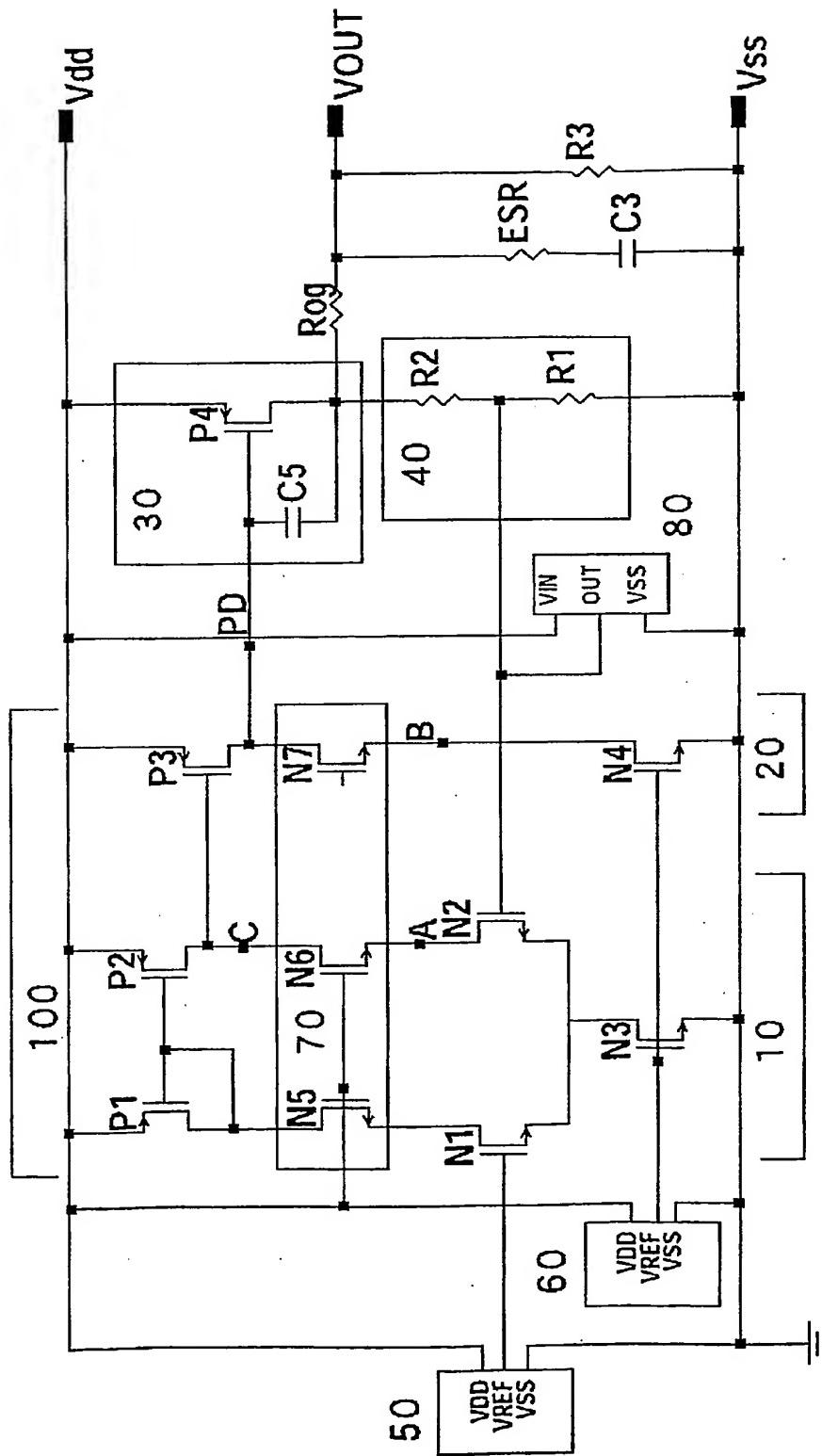
図 1-5



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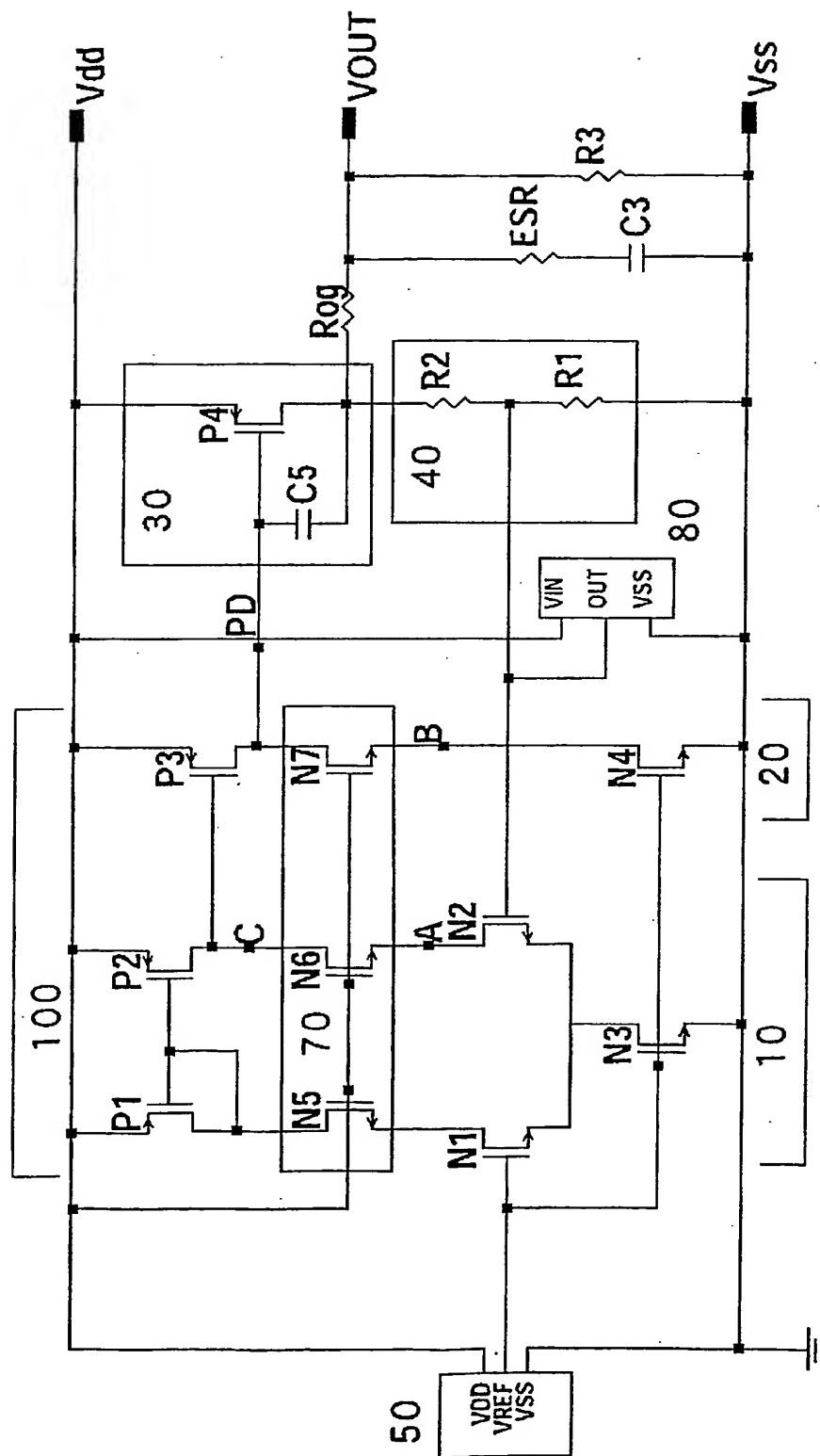
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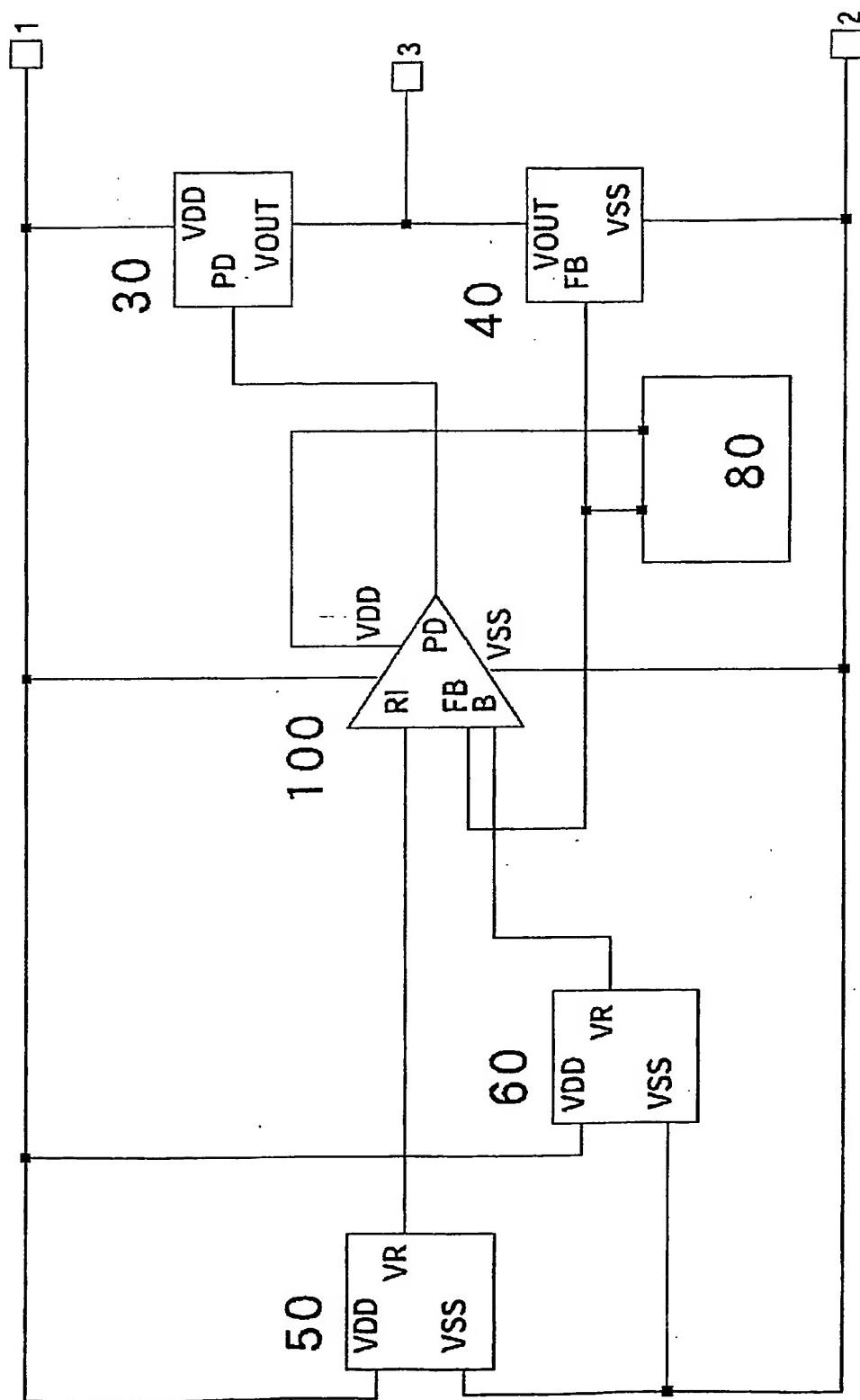
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第17図



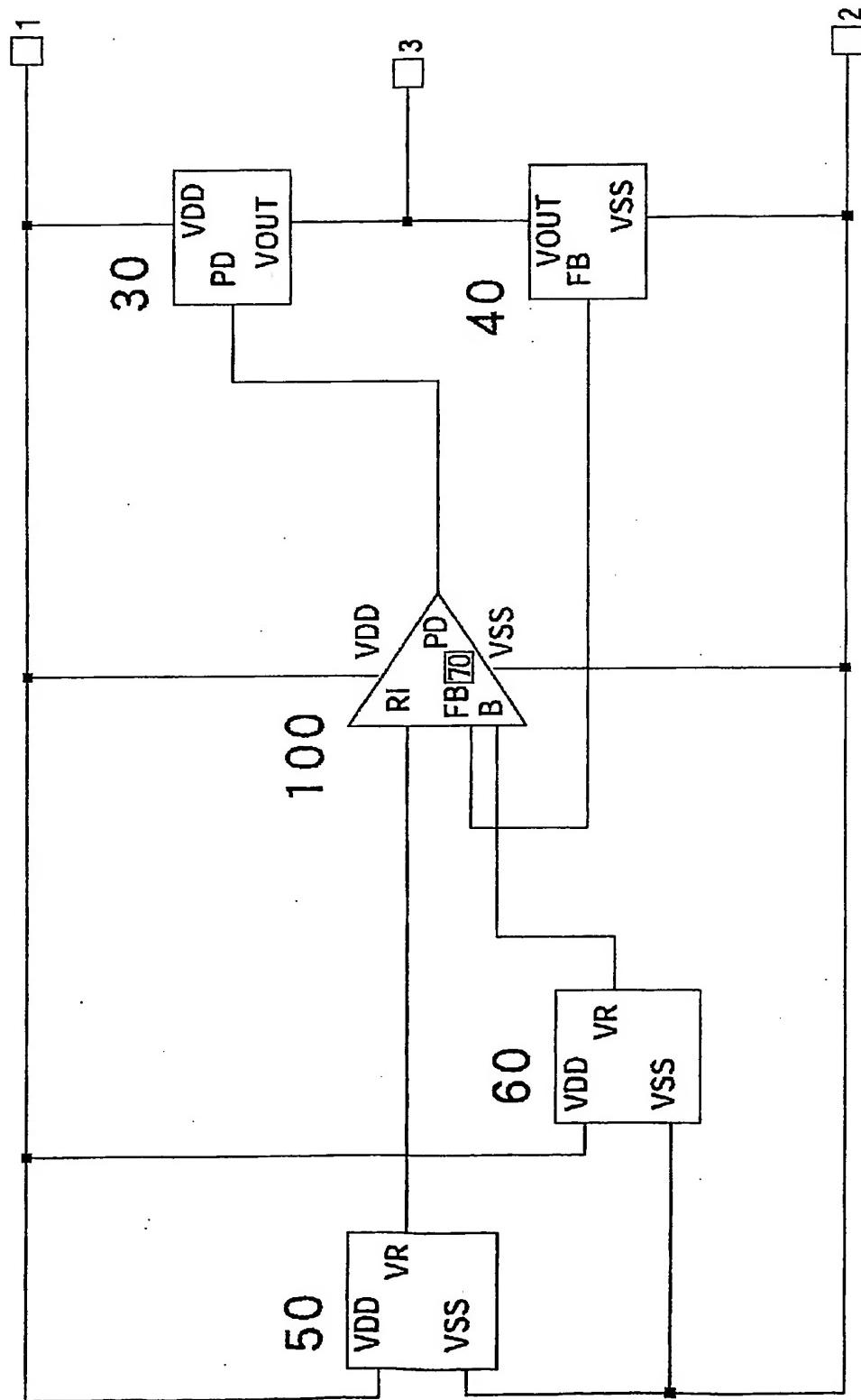
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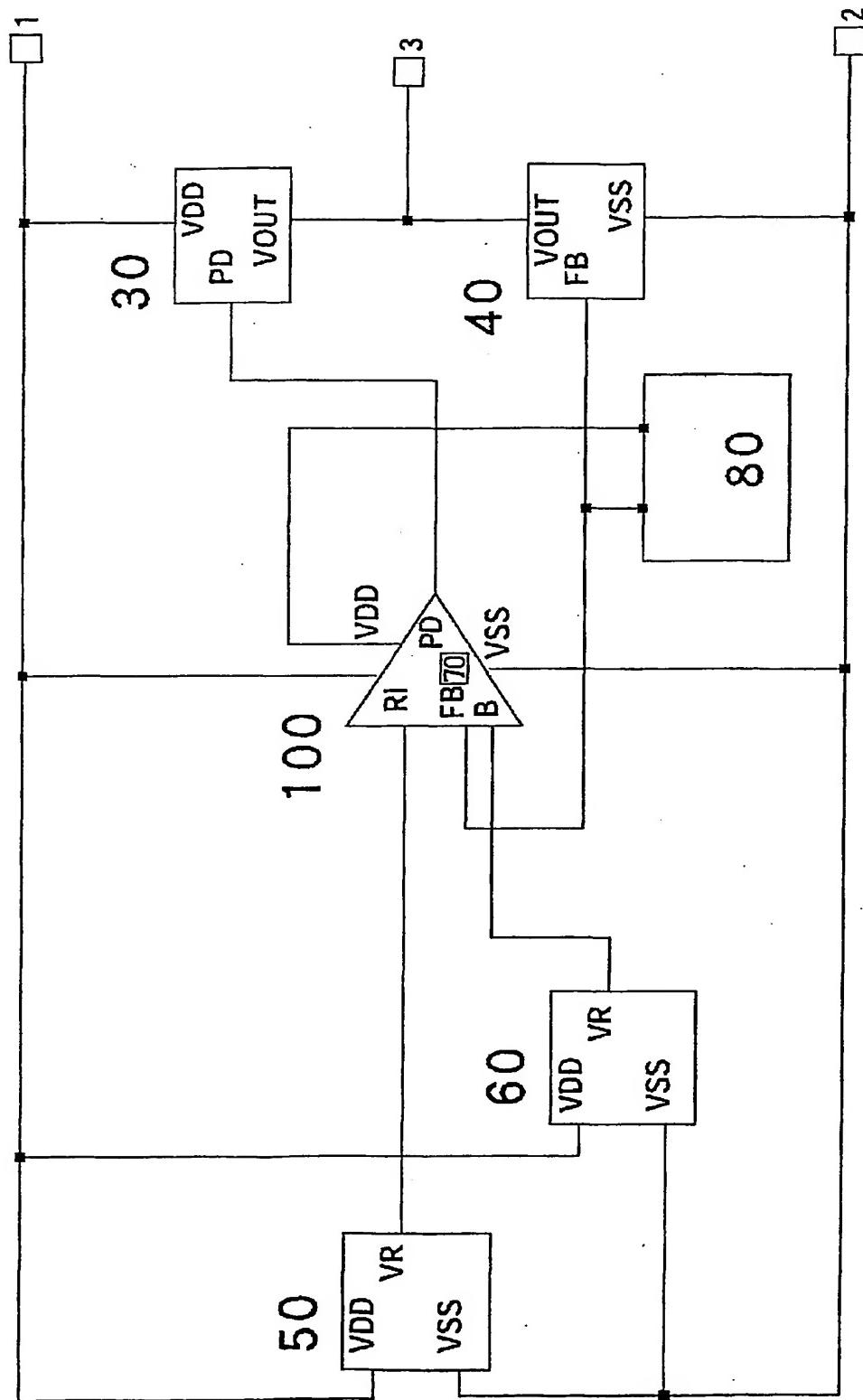
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第19図



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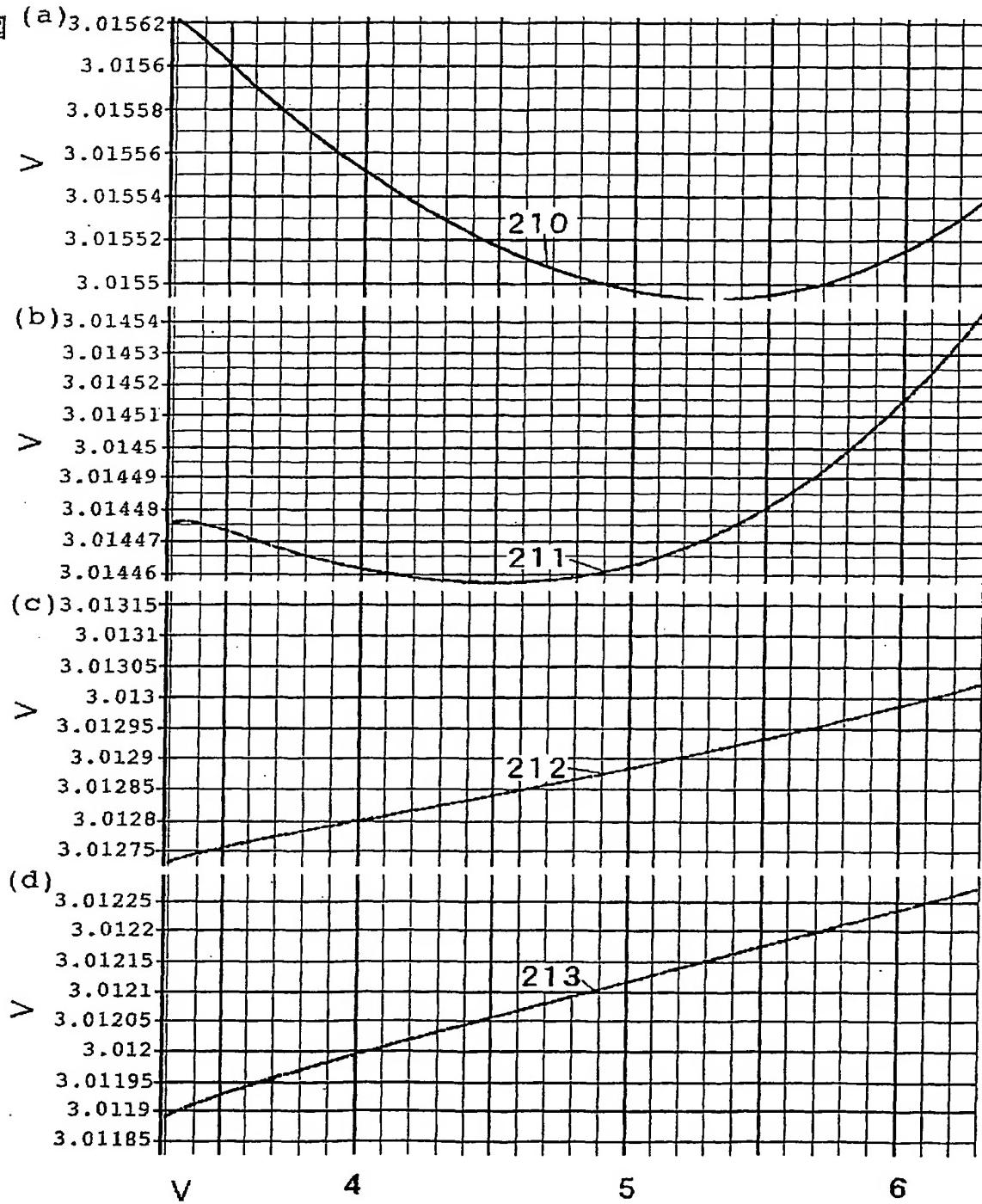
第20図



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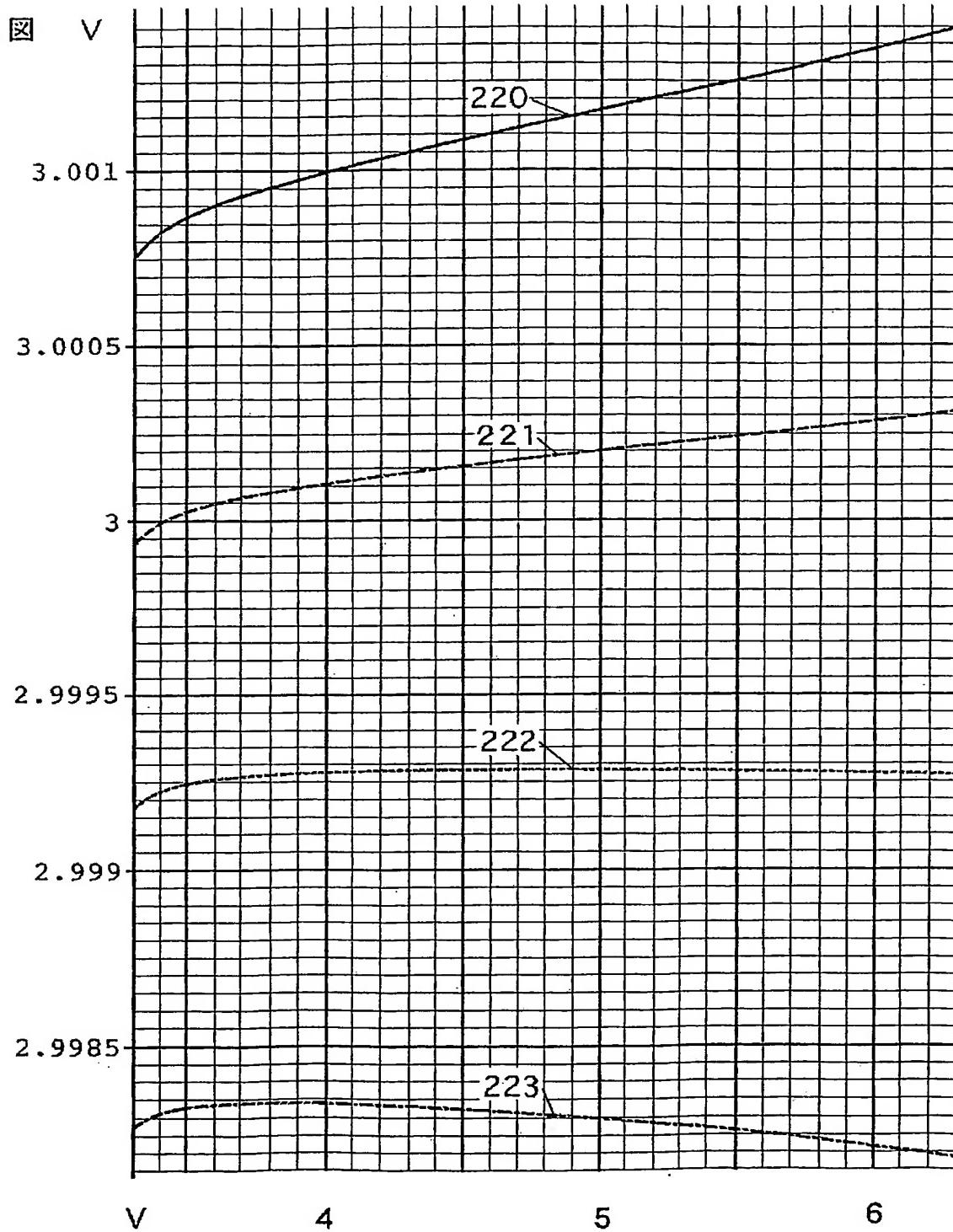
第21図



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第22図



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